



SRI VENKATESWARA COLLEGE (University of Delhi)
Benito Juarez Road, Dhaula Kuan, New Delhi- 110021
TIME TABLE – 2026-27 (Odd Semester)

Department: Electronics

Semester: I (Hons)

	1 8.45- 9.45 am	2 9.45-10.45 am	3 10.45-11.45 am	4 11.45-12.45 am/pm	12.45 – 1.05 pm LUNCH BREAK	5 01.05-2.05 pm	6 2.05-3.05 pm	7 3.05-4.05 pm	8 4.05-5.05 pm
MONDAY		DSC 1 JL ICT IR Lab	DSC 1 LAB ICT Faculty LAB JL + RH			GE I	AEC EVS 159		
TUESDAY			DSC 1 JL 11	*ECA		DSC 2 BI, DSC 3 BII Lab Electronics Lab I & II NK + HS		VAC Semester I ICT Faculty Lab	
WEDNESDAY	GE I Th RN 257		DSC 2 BII, DSC 3 BI Lab Electronics Lab I & II NK + HS			AEC EVS 158	AEC EVS 158		*ECA
THURSDAY	DSC 3 NK 257	DSC 3 NK 257		DSC 2 HS 258			DSC 1 JL 11		*ECA
FRIDAY	DSC 2 HS 251	DSC 2 HS 251		DSC 3 NK 258		GE I RN ICT IR Lab	GE I RN ICT IR Lab	GE I Practical (ICT IR Lab) RN + HY	
SATURDAY	SEC Semester I (ICT Faculty Lab) RN + SJ (1-2)					VAC Semester I HS	AEC Sem I		

Theory	DSC 1: Programming Fundamentals using Python	Prof. J Lalita (JL)
	DSC 2: Circuit Theory & Network Analysis	Dr Hari Singh (HS)
	DSC 3: Semiconductor Devices	Dr Neeru Kumar (NK)
	GE I: Data Engineering and Analytics	Dr Rakhi Narang (RN)
Practical	DSC 1: Programming Fundamentals using Python	Prof. J Lalita (JL) + Dr Rahul (RH)
	DSC 2: Circuit Theory & Network Analysis	Dr Hari Singh (HS)
	DSC 3: Semiconductor Devices	Dr Neeru Kumar (NK)
	GE I: Data Engineering and Analytics	Dr Rakhi Narang (RN) + Dr Hina Yadav (HY)
	SEC I: Basic IT Tools	Dr Rakhi Narang (RN) + Dr Sunita Jain (SJ)
	VAC I: Digital Empowerment	Dr Hari Singh (HS)

Teacher in charge



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Department: Electronics

Semester: III (Hons)

	1 8.45- 9.45 am	2 9.45-10.45 am	3 10.45-11.45 am	4 11.45-12.45 am/pm	12.45 – 1.05 pm LUNCH BREAK	5 01.05-2.05 pm	6 2.05-3.05 pm	7 3.05-4.05 pm	8 4.05-5.05 pm
MONDAY	DSC 9 RH 257	GE III / DSE III	AEC EVS 11			DSC 9 RH 251			
TUESDAY	VAC Semester III HY (ICT Faculty Lab)		DSC 8 BI Lab Electronics Lab I, 261 NJ			DSC 9 RH ICT FL	DSC 8 NJ ICT FL		
WEDNESDAY		DSC 7 TR 257	DSC 9 Lab ICT Faculty Lab RH + RN			GE III /DSE III RH ICT Faculty Lab	AEC EVS PC3	AEC EVS PC3	*ECA
THURSDAY	DSC 8 BII Lab Electronics Lab I, 261 NJ		DSC 8 NJ 251	DSC 8 NJ 251		GE III /DSE III RH ICT Faculty Lab	GE III/DSE III RH ICT Faculty Lab	GE III / DSE III Lab ICT Faculty lab RH	
FRIDAY	DSC 7 TR 256		DSC 7 Lab ICT Faculty Lab TR + JL			DSC 7 TR 251			
SATURDAY	AEC Sem III		VAC Semester III SJ			SEC Semester III TR (ICT Faculty Lab)			

Theory	Papers	Name of the teacher
	DSC-7: Engineering Mathematics	Dr Tarakeswara Rao Kaviti (TR)
	DSC-8 Analog Electronics II	Dr Nutan Joshi (NJ)
	DSC-9 Signal and Systems	Dr Rahul (RH)
	GE-III /DSE-Sem III : Artificial Intelligence and Machine Learning	Dr Rahul (RH)
Practical	DSC-7: Engineering Mathematics	Dr Tarakeswara Rao Kaviti (TR) + Prof. J Lalita (JL)
	DSC-8 Analog Electronics II	Dr Nutan Joshi (NJ)
	DSC-9 Signal and Systems	Dr Rahul (RH) + Dr. Rakhi Narang
	GE-III /DSE-Sem III: Artificial Intelligence and Machine Learning	Dr Rahul (RH)
	SEC-III: Front End Web Design	Dr Tarakeswara Rao Kaviti (TR)
	VAC-III: Digital Empowerment	Dr Hina Yadav (HY) + Dr Sunita Jain (SJ)

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TIME TABLE – 2026-27 (Odd Semester)

Department: Electronics

Semester: V (Hons)

	1 8.45- 9.45 am	2 9.45-10.45 am	3 10.45-11.45 am	4 11.45-12.45 am/pm	12.45 – 1.05 pm LUNCH BREAK	5 01.05-2.05 pm	6 2.05-3.05 pm	7 3.05-4.05 pm	8 4.05-5.05 pm
MONDAY	GE V	DSE HY 257	DSE HY 257			DSC 14 NJ 257	DSC 14 NJ 257		
TUESDAY	DSC 15 RN 257	DSC 15 RN 257	DSC 13 Lab ICT Faculty Lab SJ + TR			DSC 13 SJ 257			
WEDNESDAY	DSE V Lab ICT Faculty Lab HY + NJ		DSC 14 NJ 251			GE V Th SJ 257	GE V Th SJ 257	GE V Practical (ICT faculty Lab) SJ	
THURSDAY	GE V Th SJ ICT Faculty Lab	DSC 13 SJ ICT Faculty Lab	DSC 15 Lab ICT Faculty Lab RN + NK			DSC 15 RN 257			*ECA
FRIDAY	DSC 14 Lab ICT Faculty Lab NJ + HY		DSE HY 251	DSC 13 SJ 251		SEC Semester V (ICT Lab)			
SATURDAY									

Theory	Papers	Name of the teacher
	DSC 13: Embedded Systems	Dr Sunita Jain (SJ)
	DSC 14: Electromagnetics	Dr Nutan Joshi (NJ)
	DSC 15: Basic VLSI Design	Dr Rakhi Narang (RN)
	DSE V: Computer Networks	Dr Hina Yadav (HY)
	GE V: Fundamentals of 8085	Dr Sunita Jain (SJ)
Practical	DSC 13: Embedded Systems	Dr Sunita Jain (SJ) + Dr Tarakeswara Rao Kaviti (TR)
	DSC 14: Electromagnetics	Dr Nutan Joshi (NJ) + Dr Hina Yadav (HY)
	DSC 15: Basic VLSI Design	Dr Rakhi Narang (RN) + Dr Neeru Kumar (NK)
	DSE-V: Computer Networks	Dr Hina Yadav (HY) + Dr Nutan Joshi (NJ)
	GE V: Fundamentals of 8085	Dr Sunita Jain (SJ)
	SEC V: Introduction to Cloud Computing (AWS)	Dr Rahul (RH) + Prof J Lalita (JL)

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VENKATESWARA COLLEGE (University of Delhi)
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TIME TABLE – 2026-27 (Odd Semester)

Department:

Semester: VII (Hons)

	1 8.45- 9.45 AM	2 9.45-10.45 AM	3 10.45-11.45 AM	4 11.45-12.45 AM/PM	12.45 – 1.05 PM LUNCH BREAK	5 01.05-2.05 PM	6 2.05-3.05 PM	7 3.05-4.05 PM	8 4.05-5.05 PM
MONDAY	DSE 2 Practical ICT Faculty Lab, NK + DSE 4 (1), 261, HY		DSE 2 337 NK	DSE 2 337 NK		DSE 1 Practical ICT Faculty Lab JL			
TUESDAY	DSE 1 336 JL	GE VII 337 TR	DSE 4 Practical, 261 HY			Dissertation/Academic Project/Entrepreneurship			
WEDNESDAY		DSE 2 Th 337	DSE 1 337 JL	GE VII 337 TR			DSE 3 337 HS	DSE 3 337 HS	*ECA
THURSDAY	DSE 4 261 HY	DSE 3 337 HS	DSE 3 337 HS	DSE 4 261 HY		DSE 1 305 JL	GE VII Practical ICT Faculty Lab TR		GE VII 337 TR
FRIDAY	SBC/ Dissertation/Academic Project/Entrepreneurship					Dissertation/Academic Project/Entrepreneurship			
SATURDAY	Dissertation/Academic Project/Entrepreneurship					Dissertation/Academic Project/Entrepreneurship			

Theory	Papers	Name of the teacher
	DSE 1: Neural Networks and Deep Learning	Prof. J Lalita (JL)
	DSE 2: Digital Communication System	Dr. Neeru Kumar (NK)
	DSE 3: Introduction to Nanoscience	Dr. Hari Singh (HS)
	DSE 4: Control Systems	Dr. Hina Yadav (HY)
	GE VII: Internet of Things	Dr. Tarakeswara Rao Kaviti (TR)
Practical	DSE 1: Digital Communication	Dr. Neeru Kumar (NK)
	DSE 2: Introduction to Nanoscience	Dr. Hari Singh (HS)
	DSE 3: Neural Networks and Deep Learning	Prof. J Lalita (JL)
	DSE 4: Control Systems	Dr. Hina Yadav (HY)
	GE VII: Internet of Things	Dr. Tarakeswara Rao Kaviti (TR)

Teacher in charge